

414

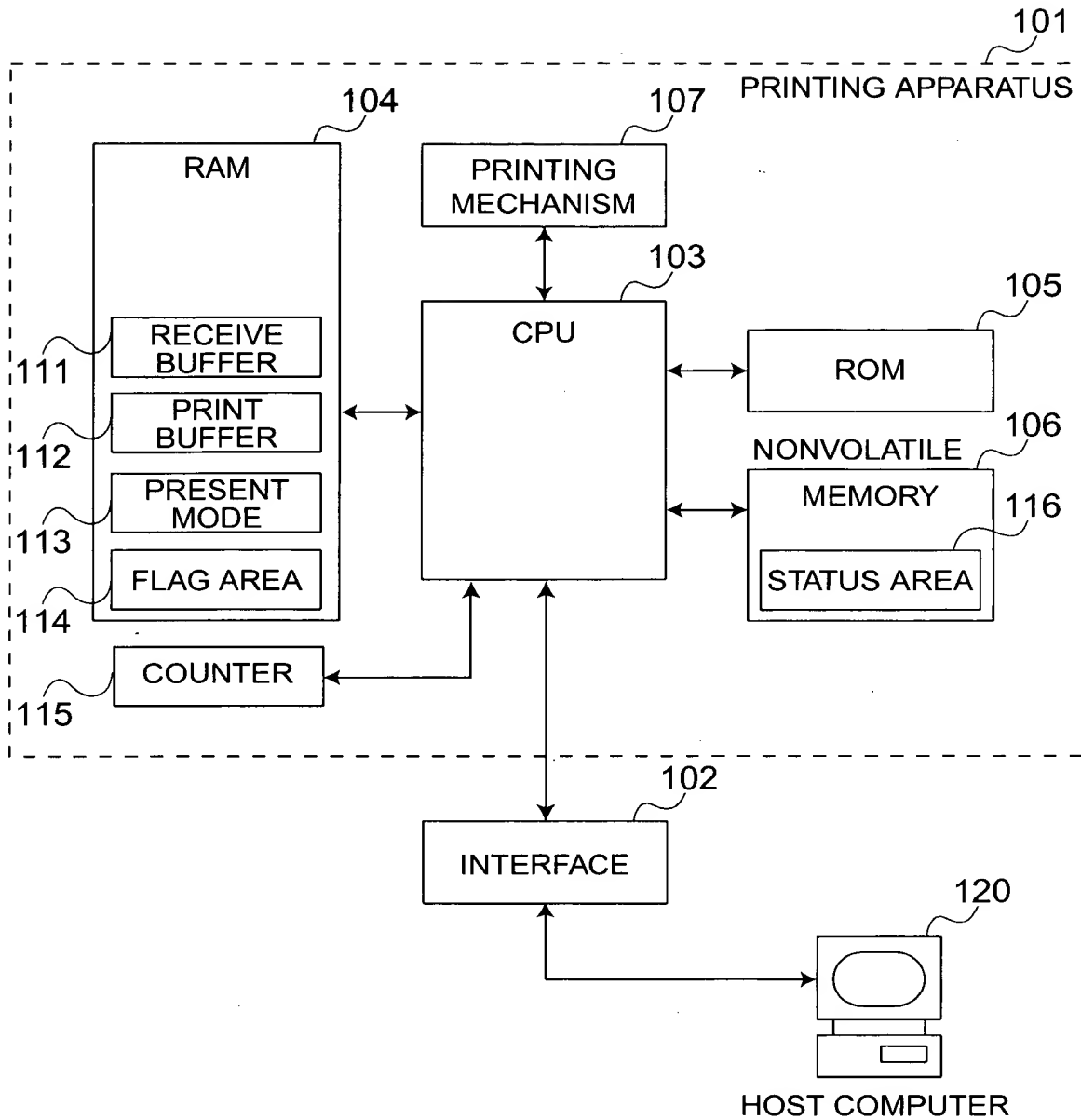


FIG. 1

0956581-032000

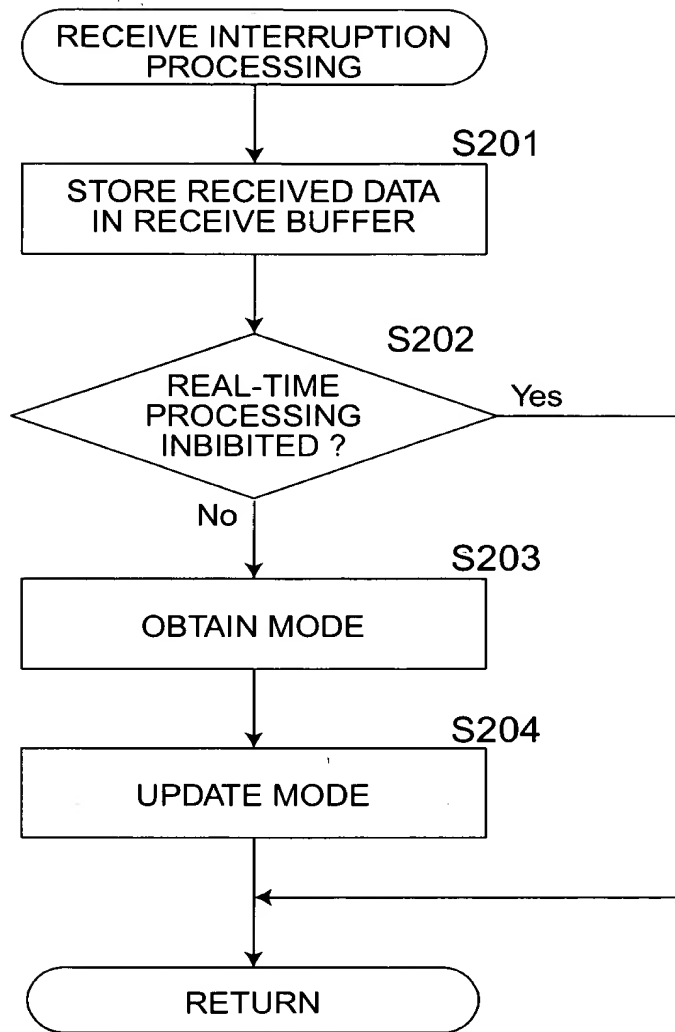


FIG. 2

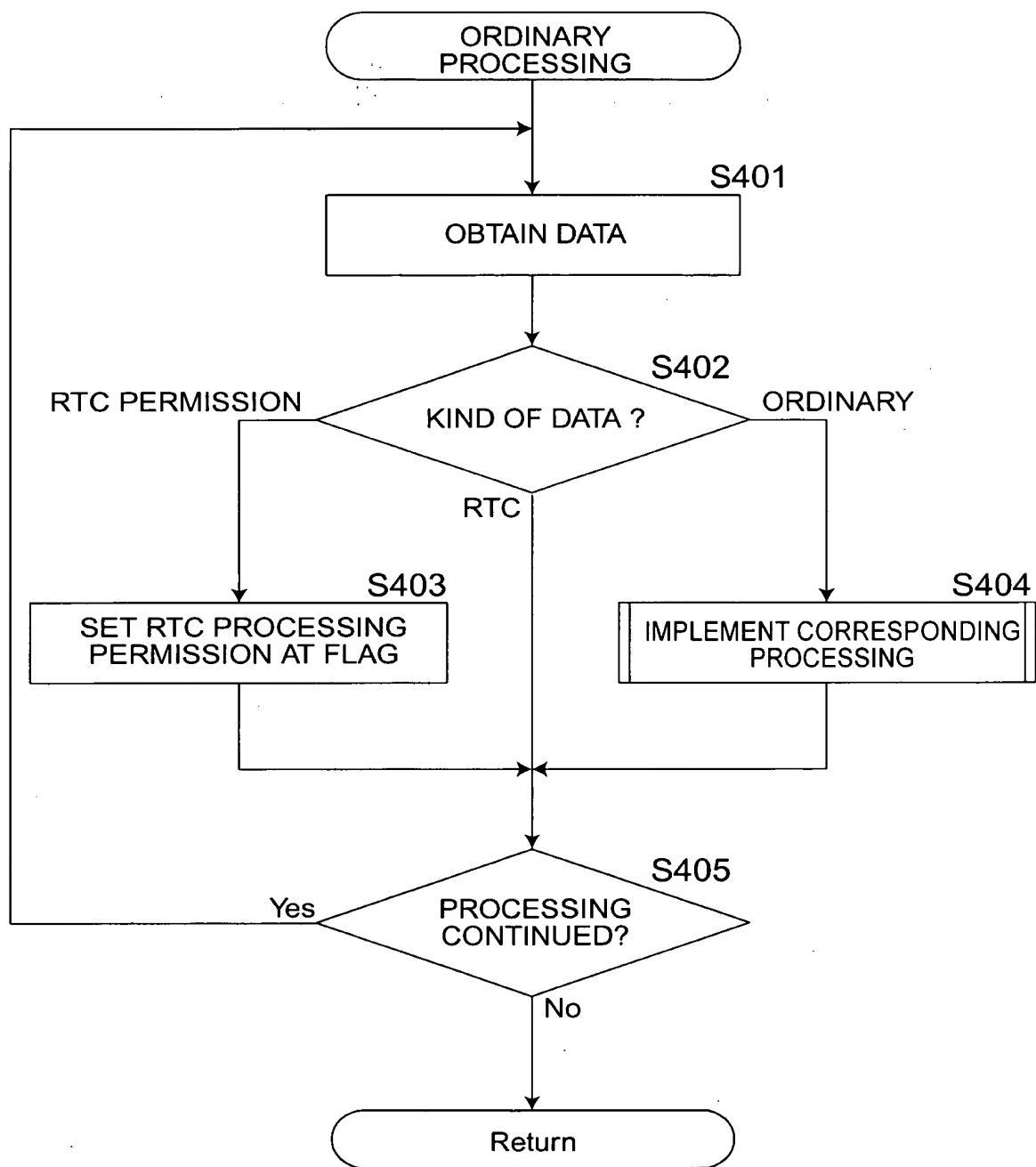
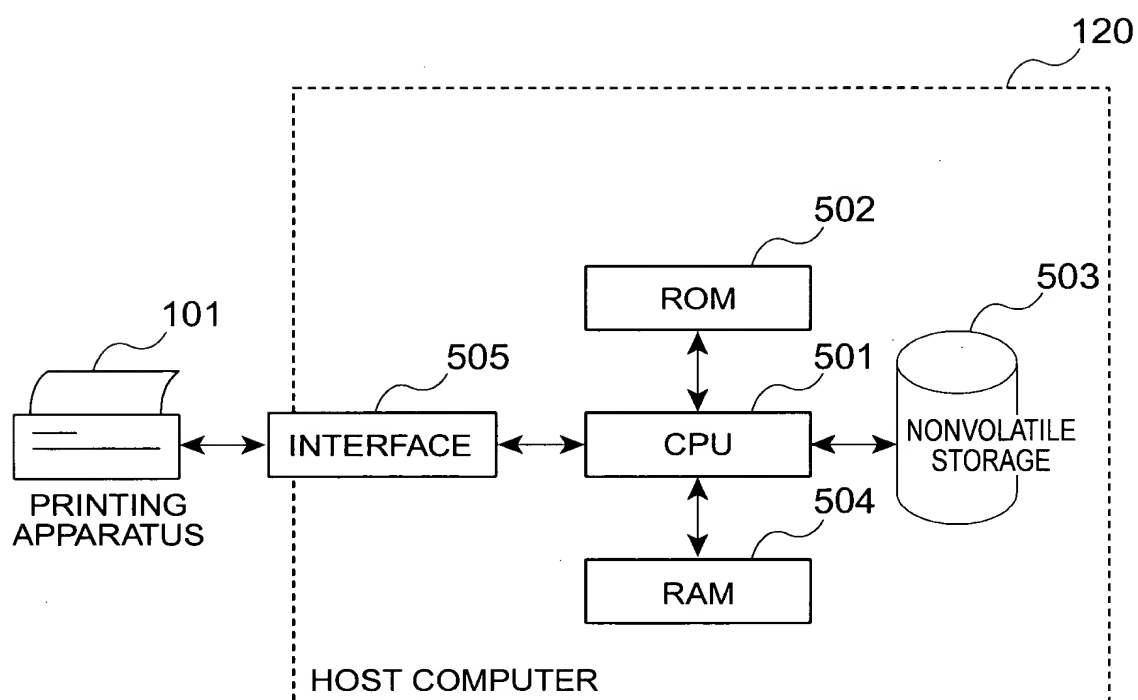


FIG. 4



00020" 18582560

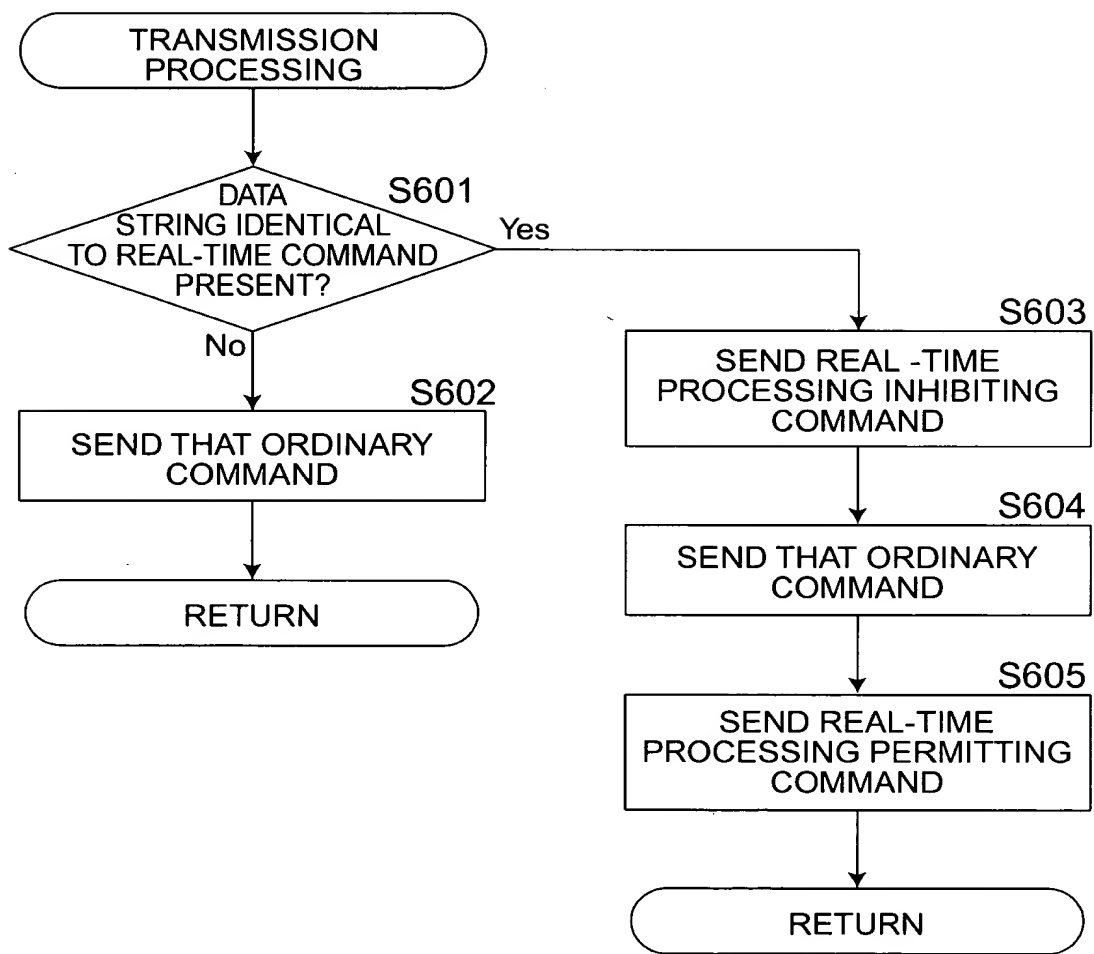


FIG. 6

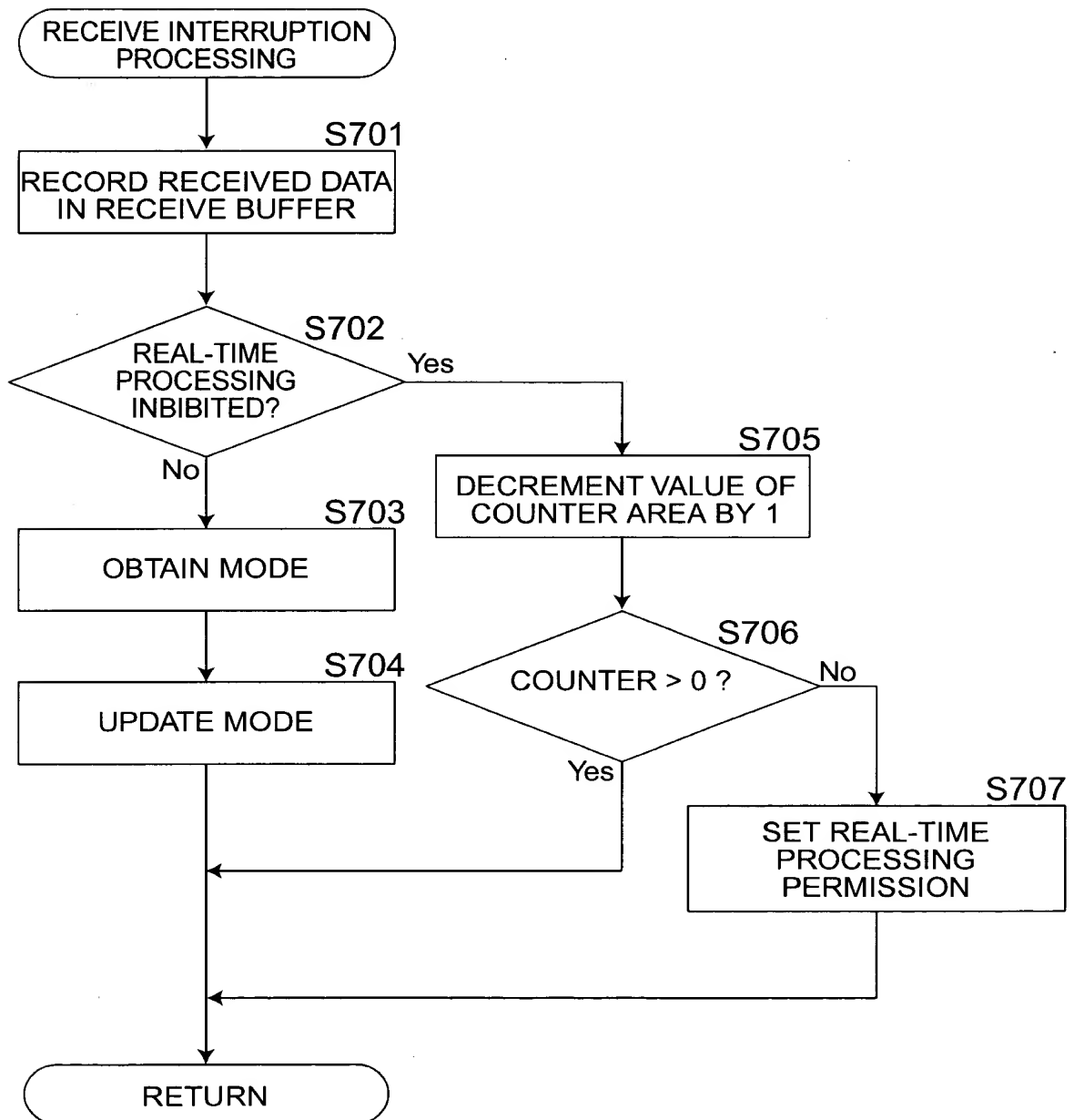


FIG. 7

09528581.032000

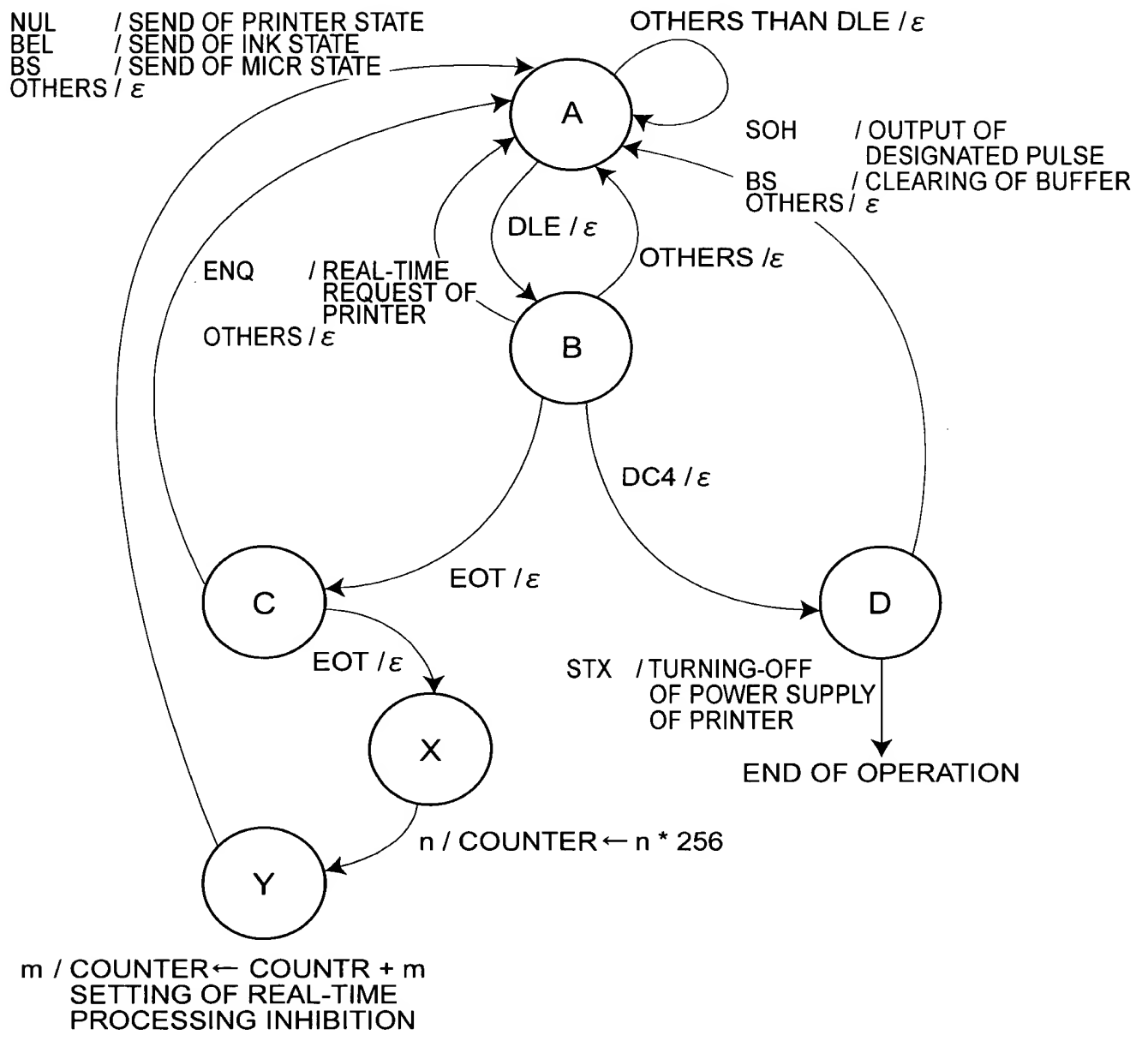


FIG. 8


```

graph TD
    Start([RECEIVE INTERRUPTION  
PROCESSING]) --> S901[S901  
STORE RECEIVED DATA  
IN RECEIVE BUFFER]
    S901 --> S902[S902  
OBTAIN MODE]
    S902 --> S903[S903  
UPDATE MODE]
    S903 --> S904{S904  
REAL-TIME  
PROCESSING NEEDED?}
    S904 -- No --> S906[S906  
PROCESS COMMAND]
    S904 -- Yes --> S905{S905  
REAL-TIME  
PROCESSING INHIBITED?}
    S905 -- No --> S906
    S905 -- Yes --> S904
    S906 --> End([RETURN])

```

FIG. 9

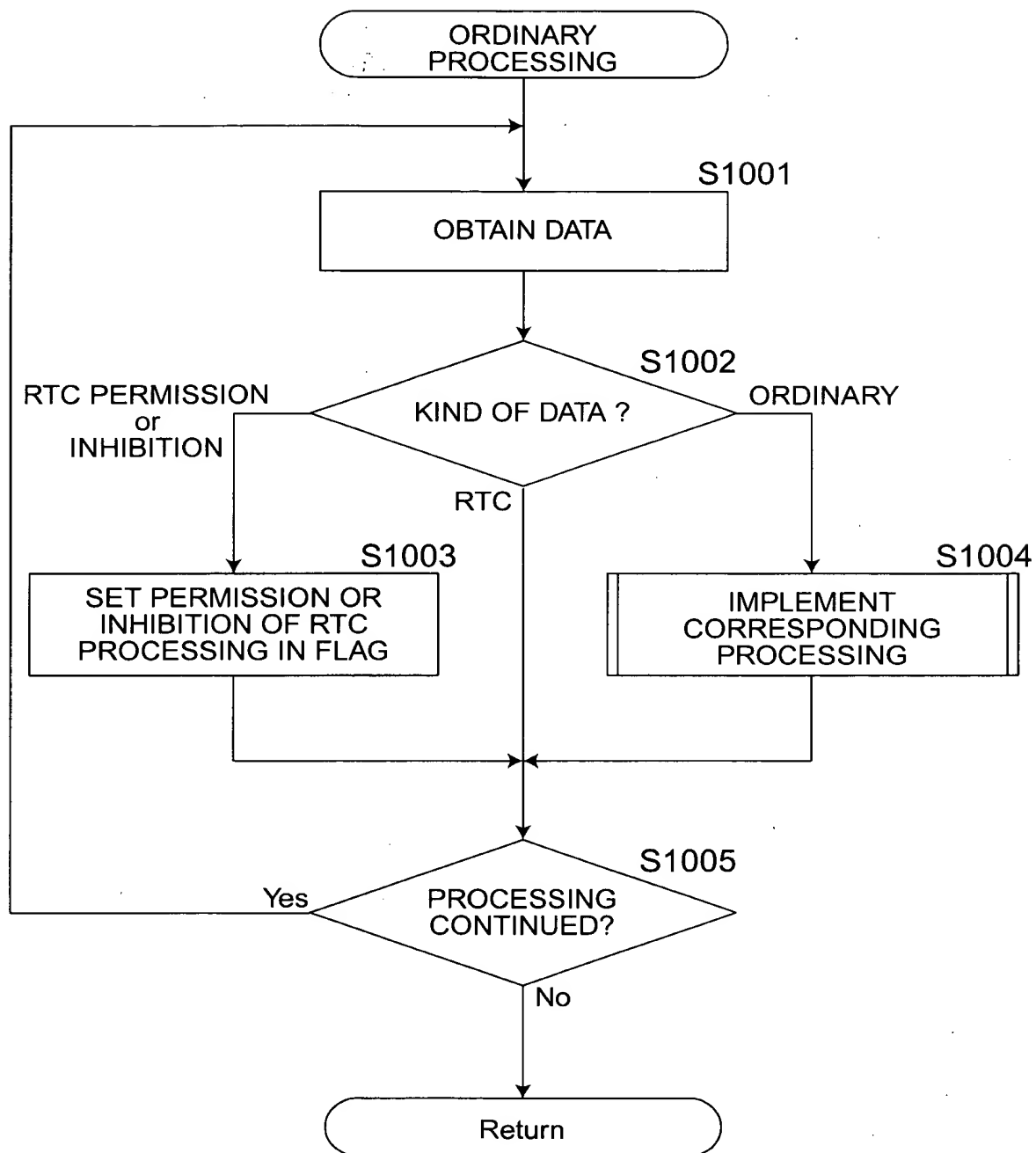
[illegible]

FIG. 10

0002ED"185B2560

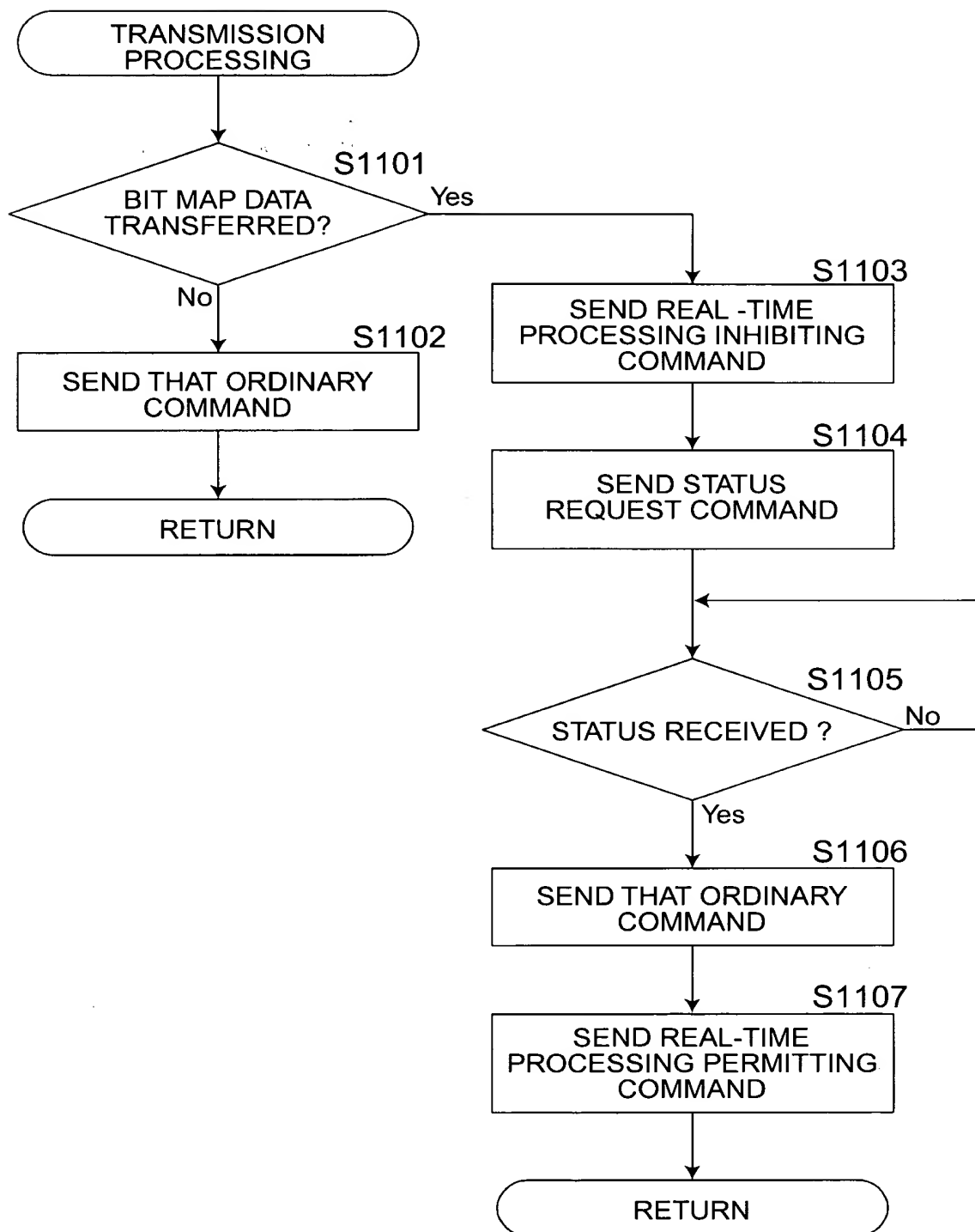


FIG. 11

000250 18582560

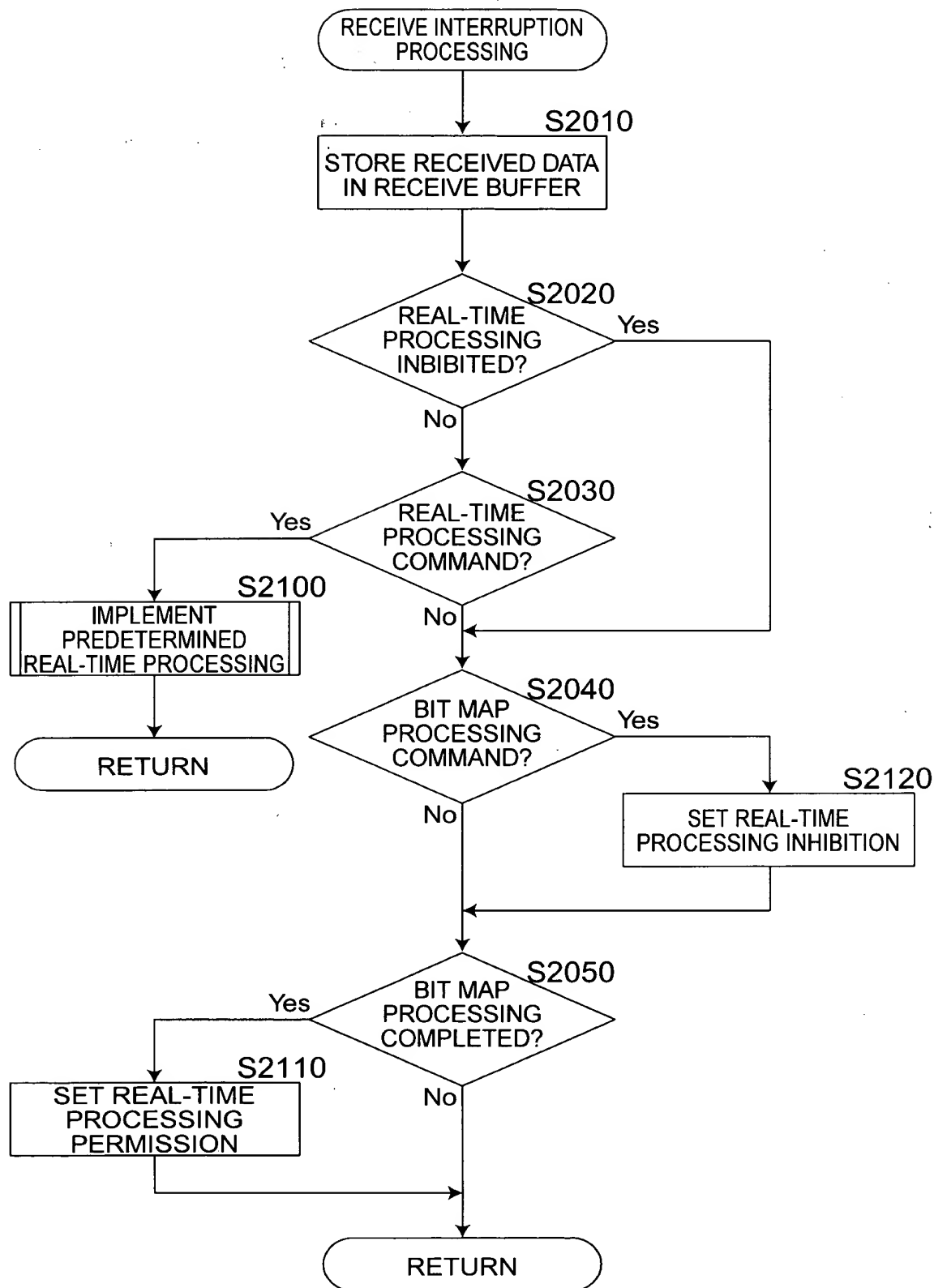


FIG. 12

```

graph TD
    A((A)) -- "DLE / ε" --> B((B))
    B -- "OTHERS / ε" --> A
    A -- "EOT / ε" --> C((C))
    C -- "ENQ / REAL-TIME REQUEST OF PRINTER, OTHERS / ε" --> A
    A -- "DC4 / ε" --> D((D))
    D -- "STX / TURNING-OFF OF POWER SUPPLY OF PRINTER" --> A
    A -- "1 / RTP INHIBITION" --> E((E))
    E -- "* / RTP INHIBITION & / RTP INHIBITION" --> A
    E -- "COMPLETION OF BINARY DATA PROCESSING / RTP PERMISSION" --> e1((e1))
    e1 -- "OTHERS / ε" --> A
    e1 -- "ESC / ε" --> e2((e2))
    e2 -- "FS / ε" --> A
    e2 -- "g / ε" --> e3((e3))
    e3 -- "OTHERS / ε" --> A
    e3 -- "OTHERS / ε" --> e1
  
```

Figure 1 is a state transition diagram for a printer control system. It features eight states: A, B, C, D, E, e1, e2, and e3. State A is the central hub. Transitions from A include: to B (DLE / ε), to C (EOT / ε), to D (DC4 / ε), to E (1 / RTP INHIBITION), and to e1 (OTHERS / ε). Transitions to A include: from B (OTHERS / ε), from C (ENQ / REAL-TIME REQUEST OF PRINTER, OTHERS / ε), from D (STX / TURNING-OFF OF POWER SUPPLY OF PRINTER), from E (* / RTP INHIBITION & / RTP INHIBITION), from e1 (OTHERS / ε), from e2 (FS / ε), and from e3 (OTHERS / ε). Transitions between other states include: e1 to e2 (ESC / ε), e2 to e3 (g / ε), and e3 to e1 (OTHERS / ε). The diagram also shows a path from E to e1 labeled 'COMPLETION OF BINARY DATA PROCESSING / RTP PERMISSION'. The final state is 'END OF OPERATION' reached from D.

FIG. 13

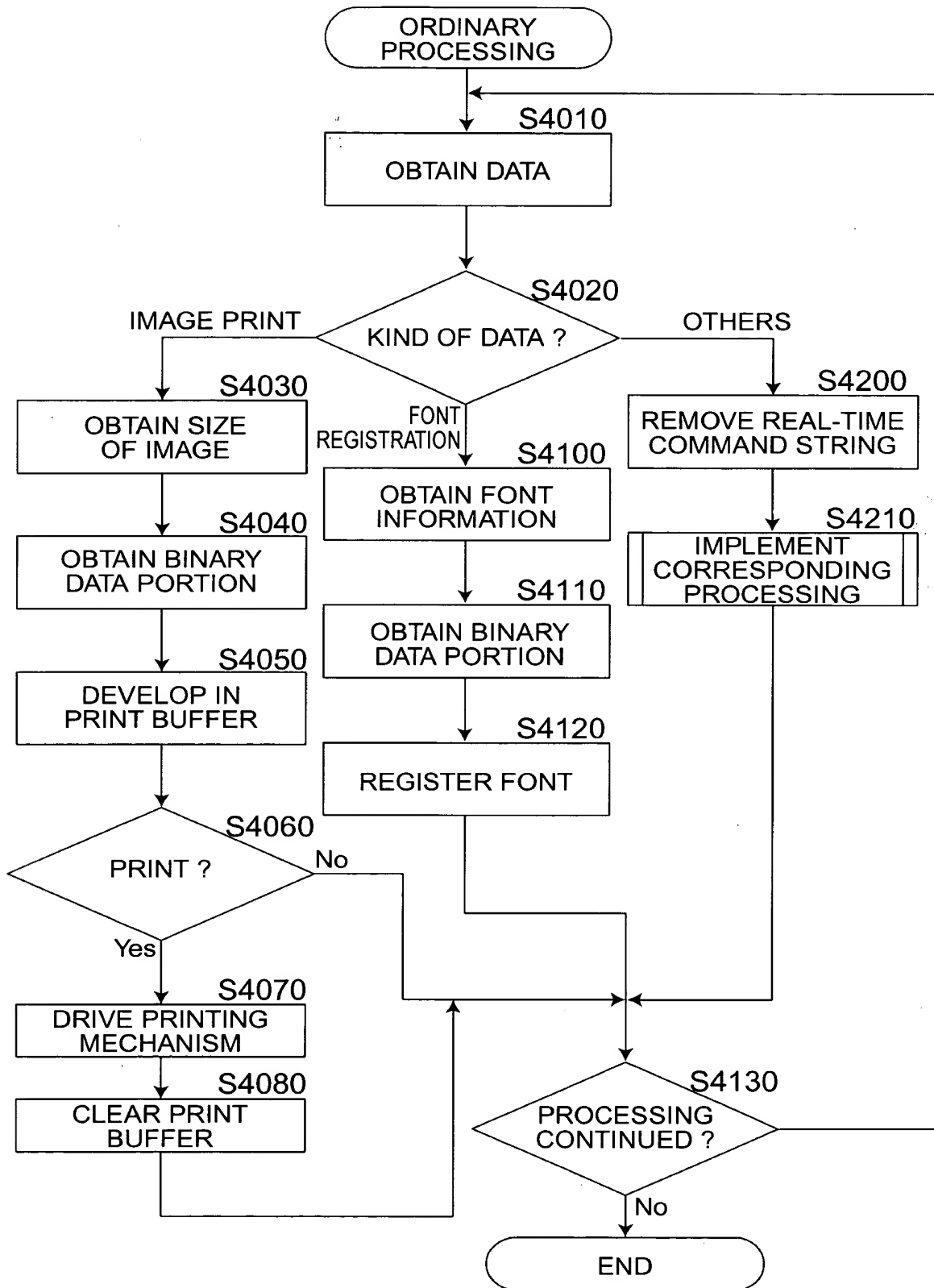


FIG. 14